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Fault-Tolerant and Testable Computing Systems. Fall 2011. Professor Daniel J. Sorin. Course Objective and Content. Objective: To provide students with an understanding of fault tolerant computers, including both the theory of how to design and evaluate them and the practical knowledge of real fault tolerant systems.

## **ECE 254 / CPS 225 - Fault-Tolerant and Testable Computing ...**

This course introduces the widely applicable concepts in reliable and fault-tolerant computing. Topics to be covered include basic testing concepts, hardware and software faults, reliability evaluation, design and evaluation of redundant systems, relationship between testing and reliability, software reliability growth, security vulnerabilities and emerging issues.

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Fault tolerant and fault testable hardware design . 1985. Abstract. No abstract available. Cited By. Angiolini F, Jamaa M, Atienza D, Benini L and De Micheli G Interactive presentation: Improving the fault tolerance of nanometric PLA designs Proceedings of the conference on Design, automation and test in Europe, (570-575) ...

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limited and a suitably designed fault-tolerant system can function even in the presence of faults. This course introduces the widely applicable concepts in reliable and fault-tolerant computing. Topics to be covered include basic testing concepts, hardware and software faults, reliability

## **CS 530 Fault Tolerant Computing**

However, a practical FFT chip is normally very big, so effective testing and fault-tolerance techniques usually are required. In this paper, we first propose a C-testable FFT network design. Only 20 test patterns are required to cover all combinational single-cell faults and interconnect stuck-at and break faults for the FFT network, regardless of its size.

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Testable and fault tolerant design for FFT networks Abstract: We propose a novel C-testable technique for the fast-Fourier-transform (FFT) networks. Only 18 test patterns are required to achieve 100% coverage of combinational single cell faults and interconnect stuck-at faults for the FFT network.

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